

INDIAN INSTITUTE OF SCIENCE EDUCATION AND RESEARCH THIRUVANANTHAPURAM [IISERTVM]

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IISER/PUR/0897/RP-P/SP/25-26

08 Oct 2025

CORRIGENDUM

Sub: Supply Installation and Commissioning of Pair of Single Photonic Counting Module and time tagger

Ref: Tender ID: 2025_IISRT_878238_1

1. The following technical specifications are added as corrigendum to the above mentioned tender

Annexure: PC

Specifications for the computer to run the Time Tagger software (Optional Item 2 in BoQ):

ltem	Specifications
A computer for operating the time tagger in the quantum studies setup.	 Processor: Intel® Core™ i7 12th gen or better Operating System: Windows 11 Pro or better Memory (RAM): 16 GB (preferably with scope for future expansion) Hard-disk Storage: 512 GB SSD with option for future extension Monitor: 24 Inch HD Accessories: Keyboard, Mouse, etc. USB ports

Note: Self-assembled computers are not acceptable.

Kindly ensure these specifications are adhered to while preparing the quote.

2. The due dates are extended as follows:-

Bid submission end date: 16 Oct 2025 (1500 Hrs)

Bid opening date: 17 Oct 2025 (1530 Hrs)

3. All other Terms and Conditions remain the same. Bidders may quote accordingly

Thanking You,

Yours Faithfully

Assistant Registrar (P&S)