



# INDIAN INSTITUTE OF SCIENCE EDUCATION AND RESEARCH THIRUVANANTHAPURAM [IISERTVM]

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GST No.32AAAJI0299R1ZS

IISER/PUR/0948/PYS/IT/25-26

16.12.2025

## CORRIGENDUM

**Sub:** Supply, installation and commissioning of a high performance computing cluster (HPC) : reg

Ref: Tender ID: 2025\_IISRT\_886282\_1

1. The following technical specifications are added as corrigendum to the above mentioned tender

"Servers with 48/64 core Intel 5th generation processors which support DDR5 @ 5600 MT/s memory type and Watts/core is less than 8 are also allowed" (for compute nodes, master/login nodes and gpu servers).

2. The due dates are extended as follows:-

Bid submission end date: 22.12.2025 (1500 Hrs)  
Bid opening date: 23.12.2025 (1530 Hrs)

3. All other Terms and Conditions remain the same. Bidders may quote accordingly

Thanking You,

Yours Faithfully

*Ammaan Soodath*  
16/12/25

Assistant Registrar (P&S)

