

IISER/PUR/0023/SI-P/SP/26-27

29.09.2026

CORRIGENDUM-Technical Specification

Subject: Supply, Installation and Commissioning of U Rack Mountable Server- change in description of Goods.

Ref: Tender Enquiry No. 2026_IISRT_927202_1

Based on the queries received the following points may be noted:-

1. The existing facility HPC configuration is as follows

Master Node	
Processor	2 X Intel® Xeon® Silver 4210R Processor 2.4Ghz 10C/20T 13.75M Cache
Memory	4 X 32GB DDR4 2933 ECC Reg-Total = 128GB RAM In Balanced Configuration
SSD	2 X 960GB SATA 6Gb/s 2.5" in Mirrored Configuration for OS
Hard Disk	5 X 8TB SATA Enterprise Class 7.2K 12Gbps 3.5" On RAID 5
HDD Bays	8 X 3.5" hot-swap SAS3/SATA drive bay
Network	2 X GbE (using i350-AM2)
RAID	RAID Controller With 8 SAS/SATA Ports; Supports RAID Levels 0,1, 5,6
VGA & Ports	1 Onboard VGA & USB 3.0 (4 rear)
IPMI Management	IPMI 2.0, shared Supports KVM over LAN, Virtual Media & Remote
Power Supply	2U Rack Mountable Server With 800W Titanium Redundant Power Supply
Operating System	Open Source Cent OS
Compute Node	
Processor	2 xIntel® Xeon® Gold 6248R Processor 3.00Ghz 24C/48T 35.75M Cache
Memory	12 X 16GB DDR4 2933 ECC Reg-Total = 192GB RAM In Balanced Configuration (4GB Per Core)
Hard Disk	2TB SATA Enterprise Class 7.2K 12Gbps 3.5"
HDD Bays	8 X 3.5" hot-swap SAS3/SATA drive bay
Network	2 X GbE (using i350-AM2)
VGA & Ports	1 Onboard VGA & USB 3.0 (4 rear)
IPMI Management	IPMI 2.0, shared Supports KVM over LAN, Virtual Media & Remote
Power Supply	2U Rack Mountable Server With 600/650W Platinum Redundant Power Supply
Operating System	Open Source Cent OS

2. The Titanium-level power supply requirement (minimum 96% efficiency) shall be complied with as specified in the technical specifications. The bidder shall submit relevant OEM/manufacture datasheet or documentation confirming compliance with the specified efficiency level along with the technical bid. No separate third-party certification is required.

3. A newer-generation processor may be offered as a substitute for the specified processor, provided the proposed processor meets or exceeds the specified requirements in terms of core/thread count, clock frequency, cache, memory bandwidth, memory capacity/support, PCIe capability, power/thermal characteristics and overall computational performance, and is fully compatible with the proposed server configuration. The bidder shall submit complete technical specifications of the proposed processor for evaluation. The proposed compute nodes shall be fully integrated with the existing HPC infrastructure at IISER TVM. The bidder shall be responsible for providing, installing and configuring all necessary system software, drivers, libraries, middleware and other configuration required for seamless communication and integration between the existing and newly supplied compute nodes. Any additional software or configuration required for such integration shall be included in the scope of supply and shall not involve any additional cost to IISER TVM.
4. A 1U/2U/4U rack-mountable server may be offered as an alternative to the specified 5U form factor, provided that the proposed configuration meets or exceeds all the specified technical requirements, including processor, memory, storage, power, cooling, expansion capability and performance requirements. The bidder shall clearly indicate the proposed form factor and demonstrate compliance with the prescribed technical specifications, including the corresponding power, cooling and rack-space requirements.
5. The existing HPC system currently consists of one master node and one CPU compute node. The existing compute node is a dual-socket system with 2 × Intel Xeon Gold 6248R processors (24 cores/48 threads per processor), 192 GB DDR4 ECC Registered memory, 2 TB enterprise storage, dual 1 GbE networking and IPMI-based remote management. The master node is a dual-socket system with 2 × Intel Xeon Silver 4210R processors, 128 GB DDR4 ECC Registered memory, OS storage in RAID-1 and data storage in RAID-5, with dual 1 GbE networking and IPMI-based remote management. The existing system is running a CentOS-based Linux environment. The proposed compute nodes are intended to expand this existing CPU-based HPC system. The existing HPC software environment, including scheduler, MPI and related system software, may be modified or reconfigured as necessary to achieve effective integration and operation of the expanded cluster. The bidder shall be responsible for the necessary installation, configuration and integration of the software environment required for the functional operation of the complete HPC system.
6. All the newly proposed compute nodes under this tender shall be from the same OEM/manufacture. This does not imply that the new compute nodes shall use the same processor generation or model as the existing compute node. A newer-generation Intel Xeon processor may therefore be offered, subject to compliance with the specified technical requirements. The existing and new compute nodes may be configured with separate queues/partitions in the job scheduler, as appropriate. The bidder shall be responsible for the necessary software configuration and integration to ensure proper operation of the existing and newly supplied nodes within the HPC environment.

Bidders may note the change and submit the quotes adhering to the above changes.

Thanking You,

Yours Sincerely,



Assistant Registrar (P&S)